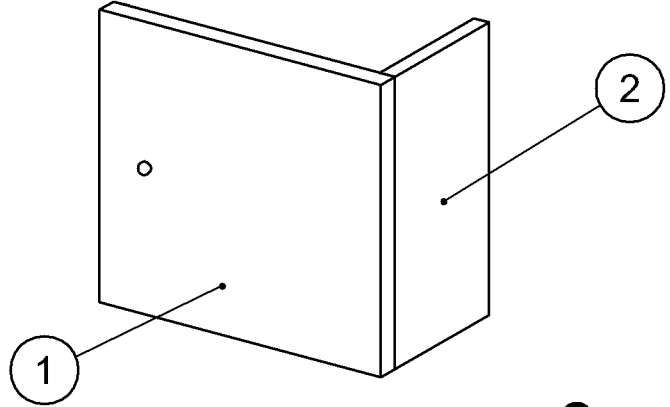
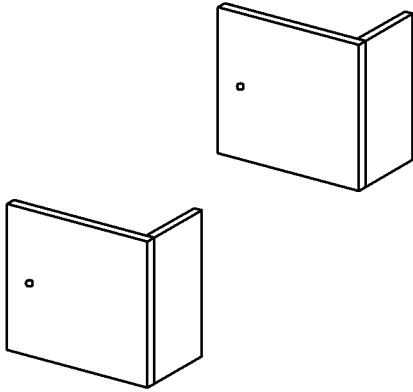
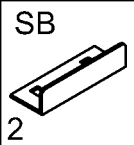
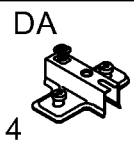
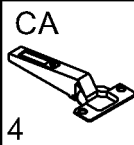
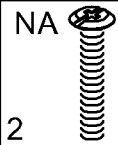
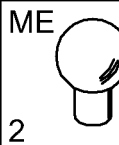
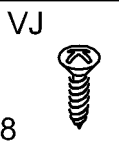
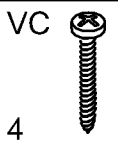
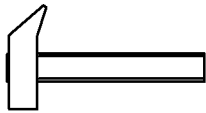


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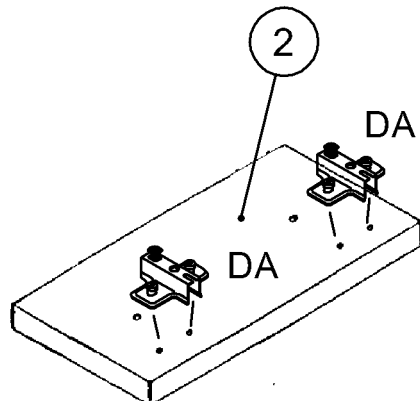
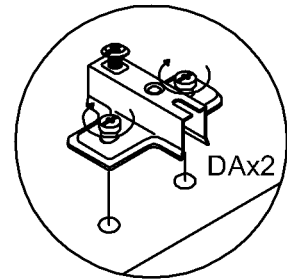
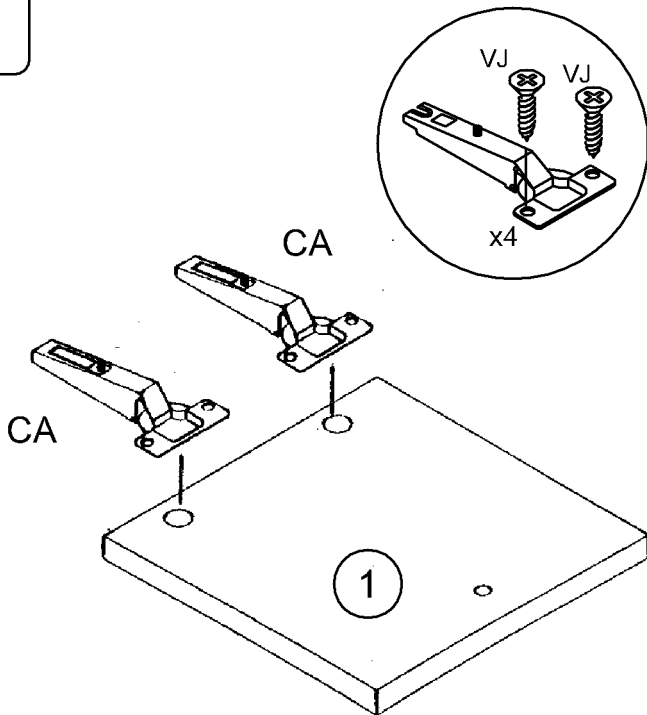
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x 2



1

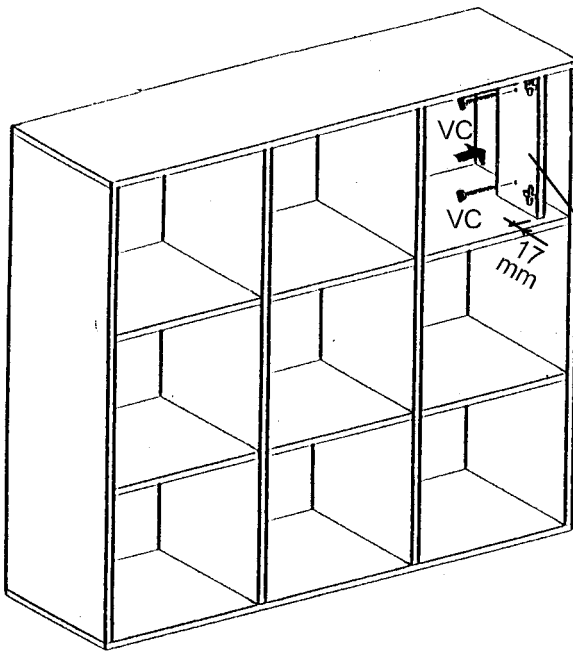


2A MAXC

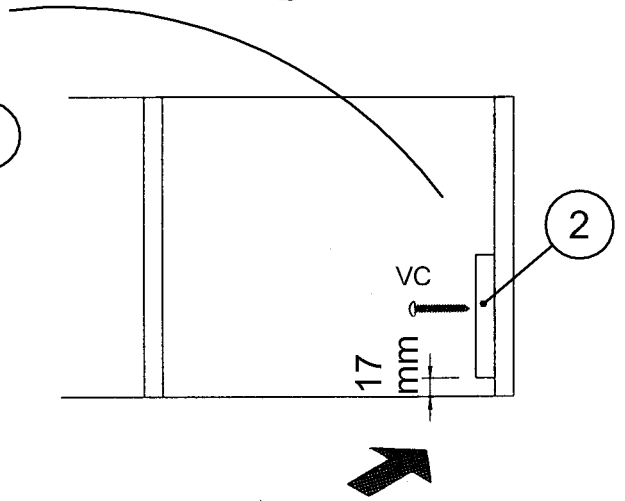
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9 MAXC
6 MAXC
3 MAXC

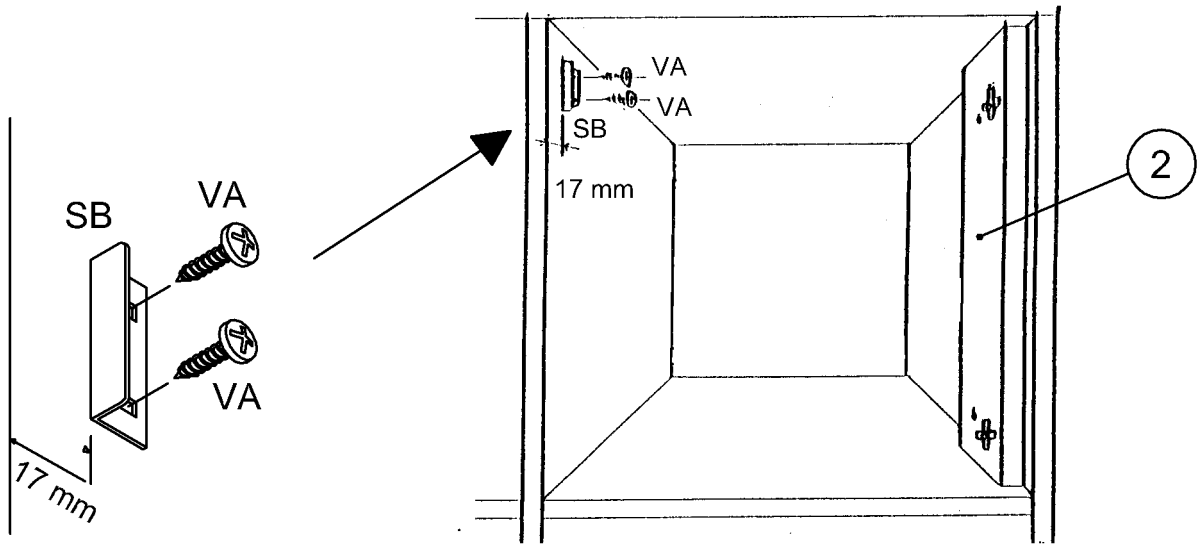
VCx2



2



3



4

